

Craig A. Moore

Process Engineer / Technician / Electrician / Instructor

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EXPERIENCE

The Guild — Electrician

November, 2017 - July 2018

San Jose, CA

Electrician employed in the design, construction and maintenance of a 3 tier, 72000W indoor agricultural facility in San Jose, California.

- Designed layout of a 3 phase, high-leg delta power service for 240V lighting and pump systems, including 120V general service and low voltage.
- Designed Motor and Controller circuits for timed lighting and pump systems.
- Conduit
- MC
- Lights
- Pumps
- Switches and Receptacles.
- Finish
- Motor and Controllers

Institute for Business and Technology — Instructor

August 2016 - October, 2017

Santa Clara, CA

Electrical Instructor in a California State accredited Apprentice Electrician certification program.

Courses instructed:

- ELEC 101: Introduction to Electrical Theory
- ELEC 201: Introduction to the NEC, Schematics, and Blueprints.
- ELEC 301: Residential Wiring Methods I
- ELEC 401: Residential Wiring Methods II
- ELEC 501: Commercial Wiring Methods
- ELEC 601: Motors, Controllers, PLC and Line Diagramming.
- ELEC 701: Photovoltaic System Design and Installation Methods

SKILLS

- ISO 9001
- MIL-STD-202
- GMP
- Six Sigma
- Process Foundation
- Process Ownership
- Authoring SOP
- Authoring Training Manuals
- Operator Training
- Automation
- Electronic Package Design
- Hermetic Sealing
- Thin-Film Circuitry
- Wafer Dicing
- Wet Chemistry Laboratory
- Electroplating
- PVD
- PECVD
- Test Tooling
- Class 100 Cleanroom
- Audits
- Hardware Maintenance
- Troubleshooting
- Repair
- Quality Assurance
- Data Analysis
- 3 phase power systems
- National Electric Code
- Layout
- MC
- Conduit
- Unistrut
- Receptacles
- Switches

Capital Valley Electric — Electrician

October, 2015 - August 2016

Menlo Park, CA

Apprentice Electrician employed on the Stanford University Project, consisting of a multi-floor, 300 unit, commercial/residential hybrid facility.

- Designed layout of a 3 phase, high-leg delta power service for 240V lighting and pump systems, including 120V general service and low voltage.
- Designed Motor and Controller circuits for timed lighting and pump systems.
- Conduit
- MC
- Lights
- Pumps
- Switches and Receptacles.
- Finish
- Motor and Controllers

- Panels
- Fans
- Lights
- Ballasts
- Motors and Controllers
- Electrical Schematics
- PLC
- Timers
- Lighting Controllers
- Pump Controllers
- Sensors
- Romex
- Liquid Tight
- Cu/Al Hardware
- Materials
- Low Voltage

Sunpreme — Research Technician

May 2013 - August 2015

Sunnyvale, CA

Hetero-junction Photovoltaic Research and Development, with a specialization in sputtered film deposition, wet chemistry laboratory, Ag to Cu cell architecture conversion, electroplating, and process scaling to overseas manufacturing in Xiajing, China.

- Perkin Elmer 4400 PVD process tuning.
- Applied Materials Precision 5000 PECVD process tuning.
- Wet Chemistry Laboratory
- Process Scaling
- GMP
- Data Analysis
- New Hardware
- Facilities

Achievements

- Identified several root causes of PVD defects that had been previously unexplained, leading to the elimination of unmeasurable variables in our process tuning.
- Designed SOP for dry-film lamination process.
- PVD process ownership.
- Designed wafer fixture that improved ITO film uniformity from 23% variance to 5% variance.

CTT Inc. — Process Engineer

January, 2006 - December 2012

Sunnyvale, CA

ISO 9001 manufacturer of power amplifiers, low-noise amplifiers, frequency converters, frequency multipliers, transmitters, transceivers, and receivers for end use within defense industry applications.

- Owner of Nd/Yag, TIG and Solder Seal hermetic sealing processes.
- Owner of Thin-Film Circuitry Laboratory.
- MRSI 505 Pick and Place
- ISO 9001

Achievements

- Founded a chemical process to extract atomized gold from spent etchants, reducing process cost by \$100,000 annually.
- Increased throughput of Nd/Yag hermetic sealing by 350% through efficient programming, automation and SOP design.
- Founder of ADT Profortis Wafer Dicing Process.
- Founder of Hermetic Sealing of Microelectronic Packages process.
- Increased first pass yield of Nd/Yag hermetic sealing to 99.8%.

EDUCATION

The University of California, San Diego — B.S. Chemistry

September 2000 - December 2005

Undergraduate Researcher in the Dr. White Research Group, investigating the enology and survivability of bioengineered yeast strains for end use in beer and wine making industries.